

WEST Search History

DATE: Friday, April 27, 2007

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=PGPB; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L18	l12 and L17	4
<input type="checkbox"/>	L17	(configuration near3 parameters)[clm]	1457
<input type="checkbox"/>	L16	l12 and L15	0
<input type="checkbox"/>	L15	(configurable near3 ((source adj code) or code))[clm]	57
<input type="checkbox"/>	L14	L13 and (reuse or "re-use")[clm]	0
<input type="checkbox"/>	L13	l12 and (soc or (system adj2 chip))	64
<input type="checkbox"/>	L12	(core near3 interface)[clm]	564
<input type="checkbox"/>	L11	L8 and (different near level)[clm]	3
<input type="checkbox"/>	L10	l8 and (level adj2 functionality)[clm]	0
<input type="checkbox"/>	L9	l8 and (level adj functionality)[clm]	0
<input type="checkbox"/>	L8	l1 and (soc or (system adj2 chip))	245
<input type="checkbox"/>	L7	(selectively adj physically adj present)[clm]	0
<input type="checkbox"/>	L6	(selectivley adj physically adj present)[clm]	0
<input type="checkbox"/>	L5	l1 and (physically adj present)[clm]	0
<input type="checkbox"/>	L4	L3 and (at adj compilation)[clm]	0
<input type="checkbox"/>	L3	l1 and configurable[clm]	95
<input type="checkbox"/>	L2	L1 and "interface signal carriers"[clm]	0
<input type="checkbox"/>	L1	core[clm] and interface[clm]	2838

END OF SEARCH HISTORY